在高速的数字互连中,信号衰减是介电损耗和导体 损耗的结果。特征与建模工作通常限于介电损耗在 印刷电路板中的影响以及不同介电材料之间的分别。 大部分高速特征建模工作通常不考虑因铜箔粗糙度 或促进铜箔粘合的处理而导致的导体损耗。证明 Hammenstad与Jensen的分析公式只对特定表面粗糙 度有效。需要进行其它工作因应表面粗糙度差异优 化模型。

# Modeling Conductor Surface Roughness

Stripline models can be used to determine the impact of copper surface roughness on transmission properties. **by ANTONIO CICCOMANCINI SCOGNA** 

In high speed digital interconnects, signal attenuation is a result of both dielectric losses and conductor losses. Many articles have shown in detail the characterization and modeling efforts related to the impact of dielectric loss in PCBs and the differences between various dielectric materials<sup>1</sup>. Nevertheless, high-speed characterization modeling efforts do not often take into account the variations in conductor losses due to copper foil roughness or treatments made to the copper foil to promote adhesion.

Two types of interconnect structures are mainly used in order to propagate signals in integrated circuits (ICs) and printed circuit boards (PCBs): microstripline and stripline. Stripline is constructed by sandwiching a metallic strip within a dielectric material. The benefit of the closed structure, compared to the microstripline, is better protection against external signals and unwanted radiation.

Stripline is one of the most popular methods to transfer signals in PCBs and ICs. In an ideal world, the conductor would be perfectly smooth and have a perfectly rectangular cross-section. In reality, this is not the case because the conductors have microscopic dips and grooves as a result of the foil manufacturing and PCB fabrication process. Due to the importance of these transmission lines, significant research has been done in the last few years to accurately analyze the transmission properties<sup>2-6</sup>. A critical aspect of this analysis is the focus on the characterization of the conductor losses due to surface anomalies on the copper traces.

Copper foils are roughened to improve adhesion between the copper and dielectric resin of the laminate material. The adhesion at the interface between the conductor and the insulator must be robust to survive the fabrication, assembly and standard usage conditions.

When the wavelength of the signal has a length comparable to the amplitude of the roughness, the attenuation increases as a function of frequency. It therefore must be taken into account when designing stripline structures. One of the best ways to estimate the incidence on the electrical performance of a stripline due to surface roughness is to consider different profile models.

### **Test Vehicle**

A cross-sectional view of the test vehicle structure, along with the geometric dimensions, is illustrated in **FIGURE 1**. It is a 400  $\mu$ m (L) long copper stripline ( $\sigma = 5.8e7$  S/m), with a width of 10  $\mu$ m and a height of 3.5  $\mu$ m. The dielectric material is FR-4 ( $\epsilon_r = 4.9$ ) and is considered loss free with the specific purpose of concentrating the attention on the conductor loss only.

CST Microwave Studio (CST MWS)<sup>7</sup> is used to perform



FIGURE 1A and 1B. Cross-sectional view of the test vehicle and geometric dimensions.

the EM numerical simulations. The code is based on the Finite Integration Technique (FIT), an integral method that can be implemented in either time domain or frequency domain.

In this case, the frequency domain solver (with tetrahedral mesh) is used for two main reasons, the structure has relatively small dimensions and the evaluation of the attenuation factor due to conductor loss ( $\alpha$ ) is not available in time domain.

In order to ensure a transversal electro-magnetic (TEM) structure of the field (essential condition for a meaningful interpretation of the S-parameters), lumped voltage sources are not suitable because they would excite higher-order modes.

Because of this, the TEM excitation has been realized by considering waveguide ports. **FIGURE 1B** illustrates the field mode pattern distribution, the line impedance value and the attenuation factor alpha ( $\alpha$ ): the detected value of the line impedance is approximately 40 ohm while  $\alpha = 40.98$  [Neper/m].

Due to the non-deterministic nature of the surface roughness<sup>8,9</sup> (different and complex geometries for the surface profile) there is always a certain level of approximation that will occur, therefore, different models should be developed depending on the surface profile type. A different approach to quantify conductor loss might be to measure the Q-factor of a quarter wavelength resonator<sup>10</sup> or to directly measure the insertion loss of a transmission line, but the effectiveness of this method relies on the accuracy of the measurements and the availability of experimental samples.

**FIGURE 2** shows the different surface roughness profiles used to investigate the incidence on the transmission proper-

ties of the considered stripline: A) cylindrical, B) triangular and C) rectangular profiles; the geometrical parameters are d, r, h = 0.5  $\mu$ m and a = 1  $\mu$ m. The surface roughness is only modelled in the top portion of the stripline in order to reduce the aspect ratio for the full-wave simulation. The relation between conductor surfaces profile (surface roughness) and loss can be defined by **EQUATION 1**.

$$\alpha_{c} = \frac{R_{s}}{Z_{0}w} \quad Np/m$$
 EQUATION (1)

where  $R_s$  represents surface resistivity,  $Z_0$  is the impedance of the transmission line and w is the trace width. Surface resistance is a material property, partially governed by surface roughness, while  $Z_0$  and w are both design parameters. Conductive loss is directly proportional to the surface resistance though the skin effect, as signals travel at the conductor surface at different depths.

Different techniques can be incorporated to generate a loss model that includes the surface roughness effect. A model can be generated by adjusting the classical skin effect conductor loss to a higher power than the square root of the frequency. Another approach is increasing the dielectric loss tangent (tan $\delta$ ) of the dielectric material, due to the fact that for frequencies higher than 1 GHz, the difference among copper types is almost linear with respect to the frequency.

Both of these approaches have some issues<sup>4</sup>. Hammerstad and Jensen proposed an empirical formula (EQUATIONS 2 and 3), derived from microstripline measurements, and used



## MODELING



FIGURE 2. Indentation profiles considered for the surface roughness.



FIGURE 3. Insertion loss comparison between Hammenstad and Jensen formulation and full wave results for FIGURE 2A.

them to model the frequency dependent loss by means of an additional loss term defined as:

$$a'_{c} = a_{c} \cdot K_{sr}$$
 EQUATION (2)  
 $K_{sr} = 1 + \frac{2}{\pi} \arctan g \left[ 1.4 \left( \frac{\Delta}{\delta_{s}} \right)^{2} \right]$  EQUATION (3)

where  $R_s$  surface resistivity,  $Z_0$  impedance of the transmission line and w, trace width. Surface resistance is a material property, partially governed by surface roughness, while  $Z_0$  and w are both design parameters.

**FIGURE 3** compares the insertion loss results due to the full wave numerical simulation for the stripline model represented in **FIGURE 2A** with the results due to the Hammerstad and Jensen analytical formulation. A good agreement between the two sets of data is observed.

At this point, all the profiles represented in FIGURE 2 are modelled and simulated by means of CST MWS and CST Design Studio is used to cascade four blocks of the 3D model in order to get the insertion loss of a 1600  $\mu$ m long stripline. The simulations have typically used 4 GB of random access memory taking on average, 50 minutes of running time on a 3.6 GHz Intel Pentium D processor. The results are reported in **FIGURE 4**.

It is noted that the level of increase in conductor losses is dependent of the shape and distribution of indentation and that the rectangular profile produces different results from the other two cases; therefore, Hammerstad and Jensen analytical formulation is only valid for a specific profile, but for more complex geometries a 3D model needs to be realized and a full wave analysis needs to be performed.



FIGURE 4. Insertion loss comparison for the different indentation profiles.

### Conclusion

The effect of the conductor surface profile (surface roughness) on a standard interconnect stripline structure was analyzed. The electrical performance of the interconnect was studied in frequency domain (S-parameters), and a consistent decrease in the transmission properties of the line have been highlighted. Hammenstad and Jensen's analytical formulation is demonstrated to be effective only for a specific profile of the surface roughness; therefore, a unique closed formulation which allows for the modeling of the real copper surface profile is not yet available. Future research will be in the direction of probabilistic approaches that appear to be well suitable for surface roughness modelling<sup>9</sup>. PCD&F

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